#### **CMLT8099**

# SURFACE MOUNT SILICON DUAL NPN TRANSISTOR



# **APPLICATIONS:**

· Small signal general purpose amplifiers

# Central semiconductor corp.

www.centralsemi.com

## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMLT8099 consists of two individual, isolated 8099 NPN silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. This device has been designed for small signal general purpose amplifier applications.

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## **FEATURES:**

- Device is Halogen Free by design
- Current I<sub>C</sub>=500mA
- Voltage V<sub>CEO</sub>=80V

MAXIMUM RATINGS: (TA=25°C)	SYMBOL		UNITS
Collector-Base Voltage	$V_{CBO}$	80	V
Collector-Emitter Voltage	$V_{CEO}$	80	V
Emitter-Base Voltage	$V_{EBO}$	6.0	V
Continous Collector Current	$I_{\mathbb{C}}$	500	mA
Power Dissipation	$P_{D}$	350	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	$\Theta_{JA}$	357	°C/W

**ELECTRICAL CHARACTERISTICS PER TRANSISTOR:** (T<sub>A</sub>=25°C unless otherwise noted)

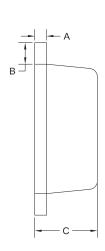
SYMBOL	TEST CONDITIONS	MÍN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =80V		0.1	μΑ
I <sub>EBO</sub>	V <sub>BE</sub> =6.0V		0.1	μΑ
$BV_{CBO}$	I <sub>C</sub> =100μA	80		V
BV <sub>CEO</sub>	I <sub>C</sub> =10mA	80		V
$BV_{EBO}$	I <sub>E</sub> =10μA	6.0		V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =5.0mA		0.4	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =100mA, I <sub>B</sub> =10mA		0.3	V
V <sub>BE(ON)</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =10mA	0.6	8.0	V
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0mA	100	300	
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =10mA	100		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =100mA	75		
$f_{T}$	V <sub>CE</sub> =5.0V, I <sub>C</sub> =10mA, f=100MHz	150		MHz
$C_{ob}$	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1.0MHz		6.0	pF
C <sub>ib</sub>	$V_{BE}$ =0.5V, $I_{C}$ =0, f=1.0MHz		25	pF

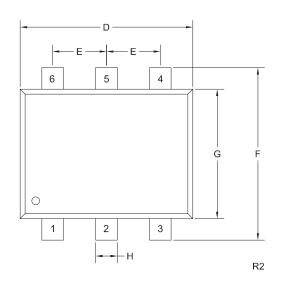
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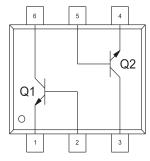


# **SOT-563 CASE - MECHANICAL OUTLINE**





#### **PIN CONFIGURATION**



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.0027	0.007	0.07	0.18			
В	0.008		0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

## LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

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R3 (29-June 2015)

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#### **CONTACT US**

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